

FIG. 1

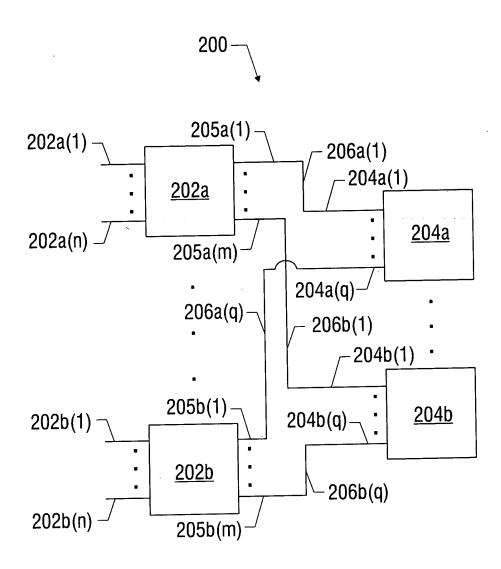
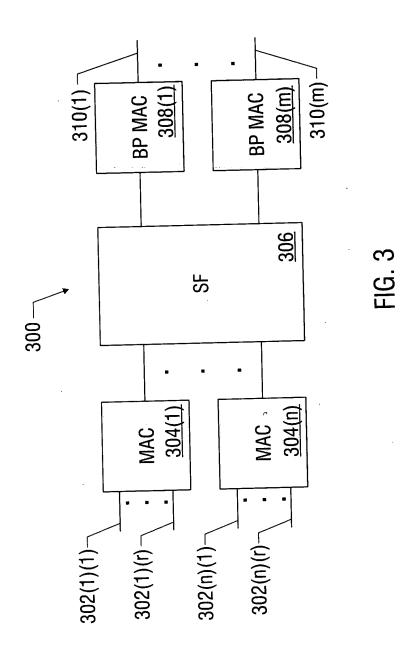


FIG. 2



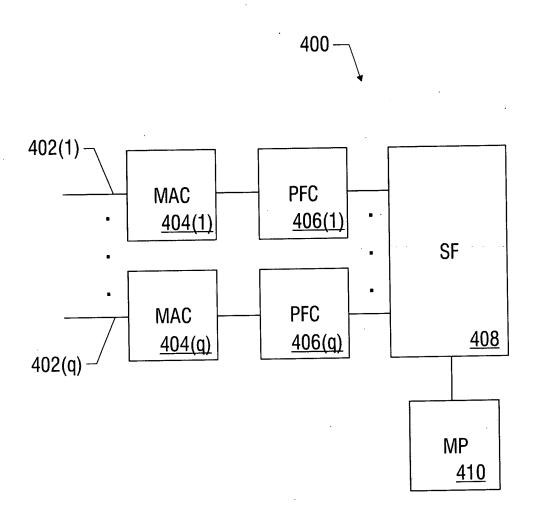


FIG. 4

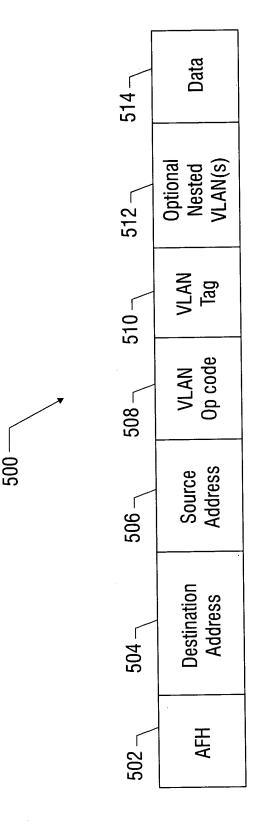
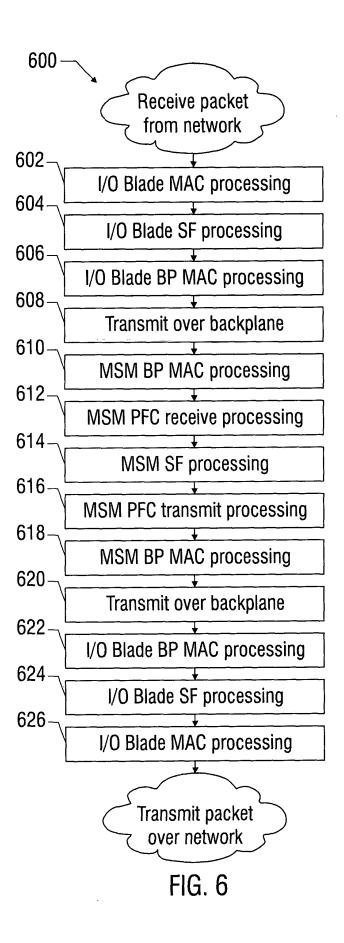


FIG. 5



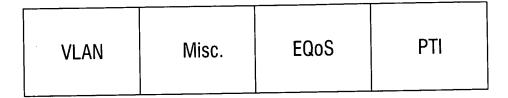


FIG. 7

Port State	Ingress Port #	Misc.	EQoS	PTI
ı			_	

FIG. 8

VLAN Port State	Misc.	EQoS	PTI
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FIG. 9

Reserved Egress Port # Misc.	EQoS	PTI
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FIG. 10

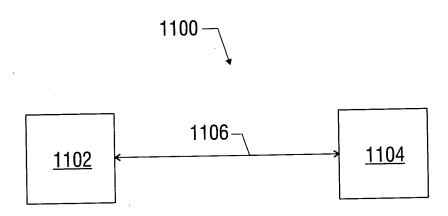


FIG. 11

12	02 \ 12	204 - 12	206 $\sqrt{}$ 12	08 - 12	210 –
	Ethertype Select (1 bit)	Port State (4 bits)	Reserved (1 bit)	VLAN State (2 bits)	Port Number (8 bits)

FIG. 12

1302 —	13	304 — 1	305 –	13	806 —	1308 –	13	310 –
Ether Sele (1 b	• •	Reserved (2 bits)	Pri	op ority bit)	VLAN Delete (1 bit)	Que Selo (3 b	ect	Port Number (8 bits)

FIG. 13

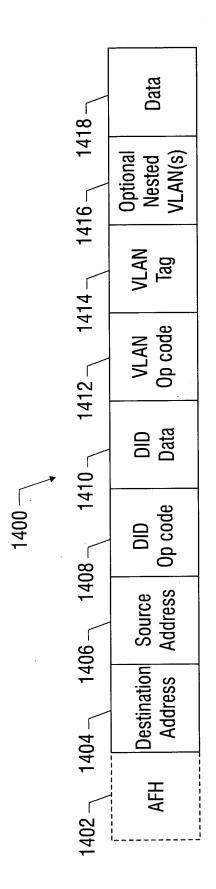
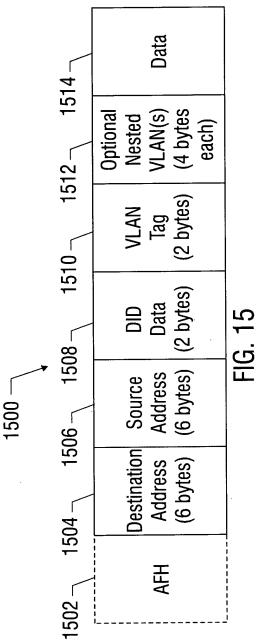


FIG. 14



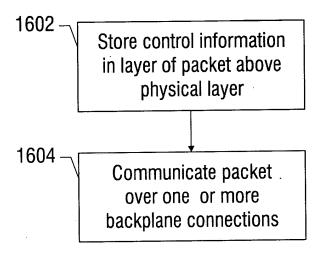


FIG. 16

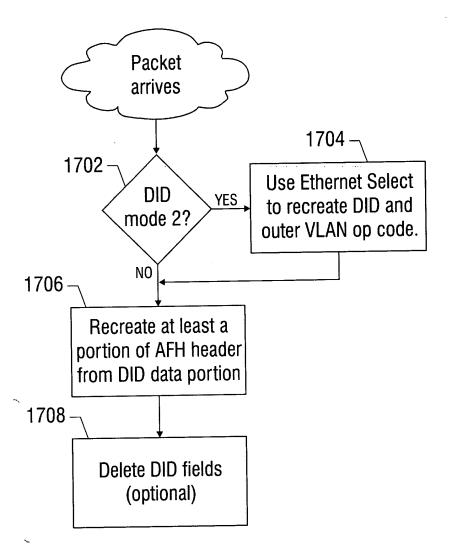


FIG. 17

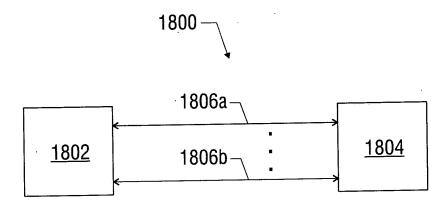


FIG. 18

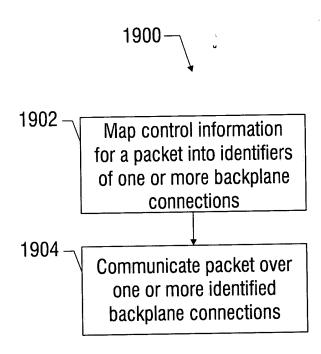


FIG. 19

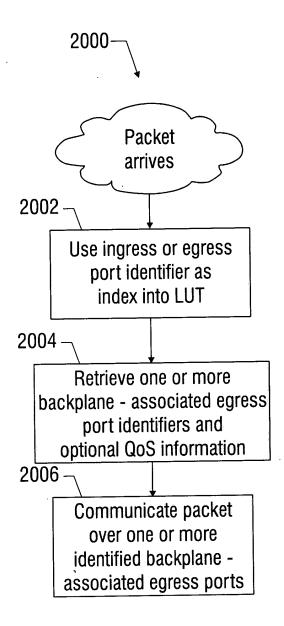


FIG. 20

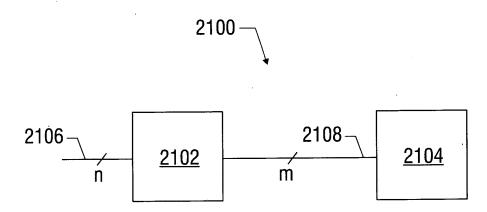


FIG. 21



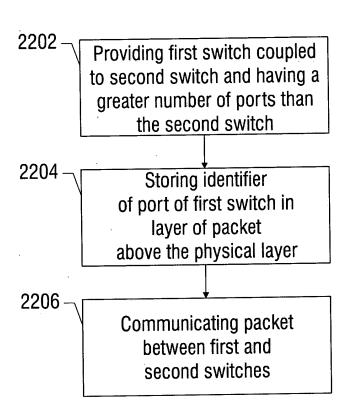


FIG. 22

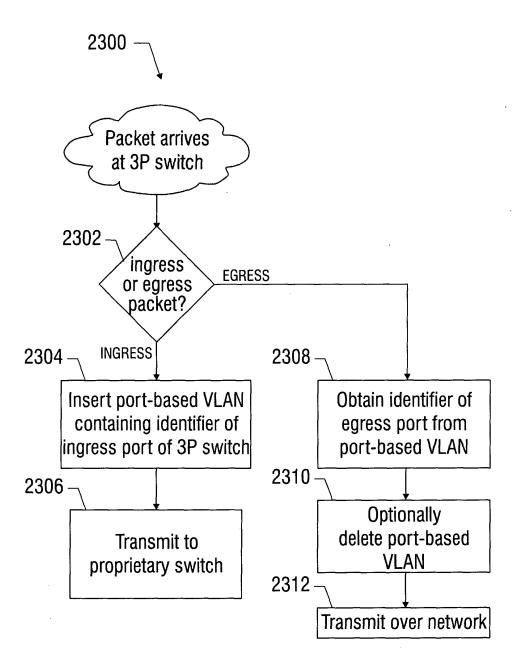


FIG. 23

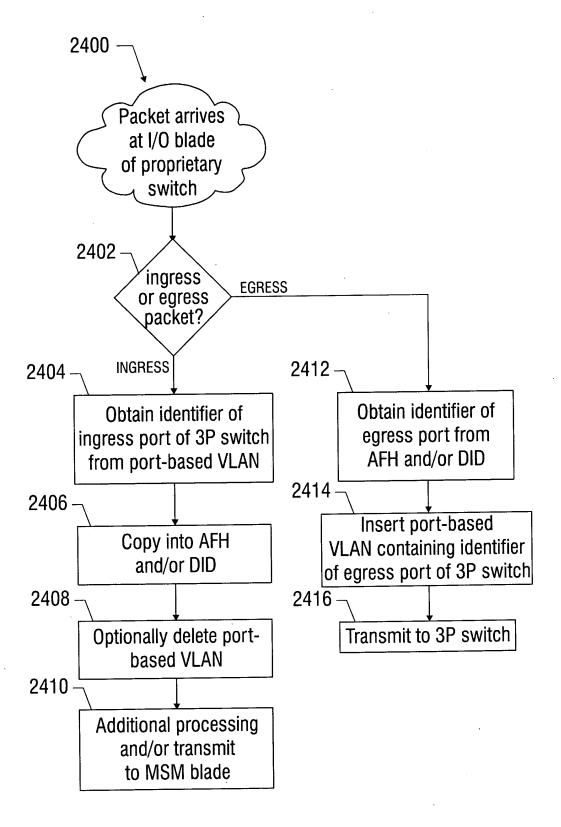


FIG. 24